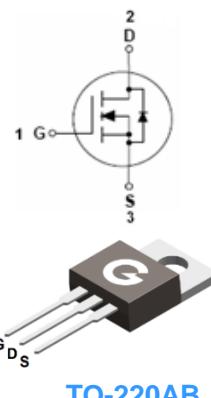


## Features

- Extremely low switching loss
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Excellent stability and uniformity

**HF**


## Mechanical Data

- Case: TO-220AB
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

## Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL033N08TH	TO-220AB	50 pcs / Tube	033N08TH

## Maximum Ratings (@ $T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DSS}$	80	V
Gate-to-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current ( $T_c = 25^\circ\text{C}$ ) <sup>5</sup>	$I_D$	200	A
Continuous Drain Current ( $T_c = 100^\circ\text{C}$ ) <sup>5</sup>	$I_D$	126.5	A
Pulsed Drain Current <sup>3</sup>	$I_{DM}$	800	A
Single Pulse Avalanche Energy <sup>3, 6</sup>	$E_{AS}$	1200	mJ

## Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation ( $T_c = 25^\circ\text{C}$ ) <sup>2</sup>	$P_D$	260	W
Thermal Resistance Junction-to-Air <sup>1, 4</sup>	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	0.5	°C/W
Operating Junction Temperature Range	$T_J$	-55 ~ +150	°C
Storage Temperature Range	$T_{STG}$	-55 ~ +150	°C

**Electrical Characteristics** (@  $T_c = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
$V_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	80	-	-	V
$I_{DS(0)}$	Zero Gate Voltage Drain Current	$V_{DS} = 68\text{V}, V_{GS} = 0\text{V}$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$R_{DS(ON)}$	Static Drain-Source On-resistance	$V_{GS} = 10\text{V}, I_D = 20\text{A}$	-	-	3.3	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	2.8	4	V
$R_g$	Gate Resistance	$V_{DS} = V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	2.25	-	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{ISS}$	Input Capacitance	$V_{GS} = 0\text{V}$	-	6800	-	pF
$C_{OSS}$	Output Capacitance	$V_{DS} = 50\text{V}$	-	2300	-	
$C_{RSS}$	Reverse Transfer Capacitance	$f = 1.0\text{MHz}$	-	470	-	
<b>Switching Characteristics</b>						
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD} = 50\text{V}$ $V_{GS} = 10\text{V}$ $R_G = 3\Omega$ $I_D = 50\text{A}$	-	31	-	ns
$t_r$	Turn-on Rise Time		-	28	-	
$t_{d(OFF)}$	Turn-Off Delay Time		-	86	-	
$t_f$	Turn-Off Fall Time		-	27	-	
$Q_G$	Total Gate-Charge	$V_{DD} = 50\text{V}$ $V_{GS} = 10\text{V}$ $I_D = 50\text{A}$	-	112	-	nC
$Q_{GS}$	Gate to Source Charge		-	22	-	
$Q_{GD}$	Gate to Drain (Miller) Charge		-	35	-	
<b>Source-Drain Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage	$I_S = 20\text{A}, V_{GS} = 0\text{V}$	-	-	1.4	V
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 20\text{A}$ $dI/dt = 100\text{A}/\mu\text{s}$	-	75	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	133	-	nC

Notes:

- The value of  $R_{0JC}$  is measured in a still air environment with  $TA = 25^\circ\text{C}$  and the maximum allowed junction temperature of  $150^\circ\text{C}$ . The value in any given application depends on the user's specific board design
- The power dissipation  $PD$  is based on  $T_J(\text{MAX})=150^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used
- Single pulse width limited by junction temperature  $T_J(\text{MAX}) = 150^\circ\text{C}$
- The  $R_{0JA}$  is the sum of the thermal impedance from junction to case  $R_{0JC}$  and case to ambient
- The maximum current rating is package limited
- The  $E_{AS}$  data shows Max. rating. The test condition is  $V_{DS} = 50\text{V}, V_{GS} = 10\text{V}, L = 0.5\text{mH}$

### Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

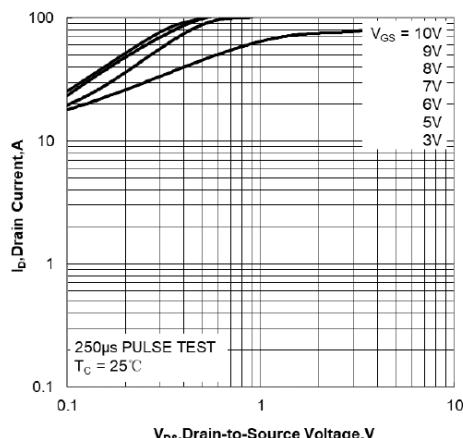


Fig 1 Output Characteristics

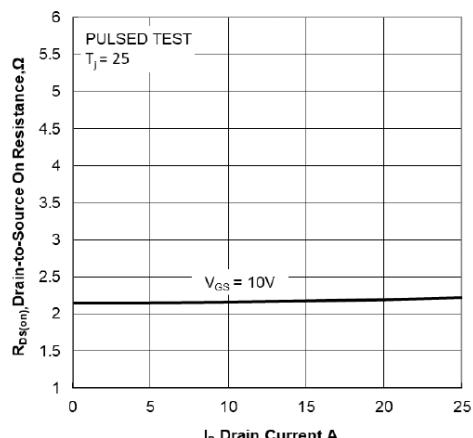


Fig 2 On-Resistance vs. Drain Current and Gate Voltage

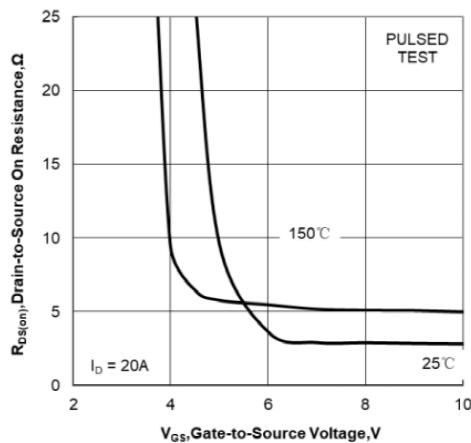


Fig 3 On-Resistance vs. Gate-Source Voltage

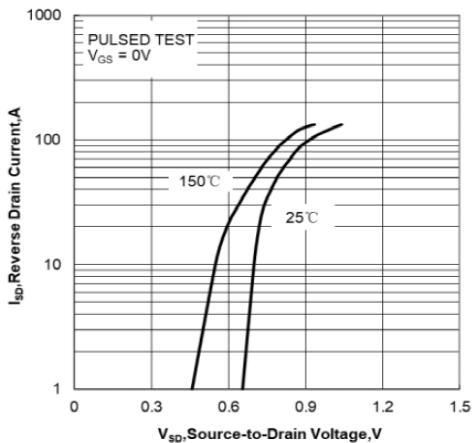


Fig 4 Body-Diode Characteristics

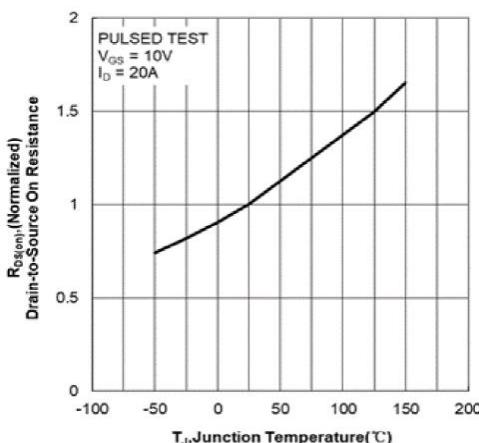


Fig 5 Normalized On-Resistance vs. Junction Temperature

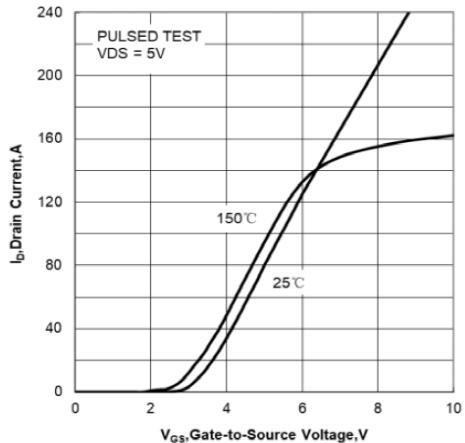


Fig 6 Transfer Characteristics

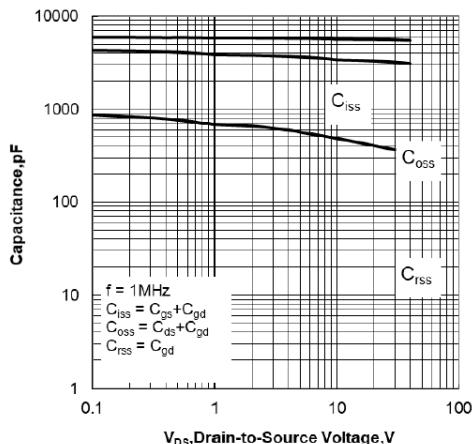


Fig 7 Capacitance Characteristics

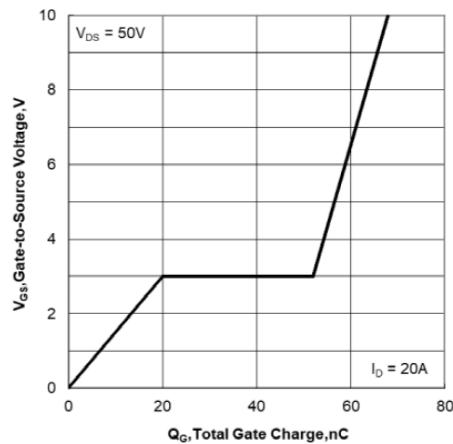


Fig 8 Gate-Charge Characteristics

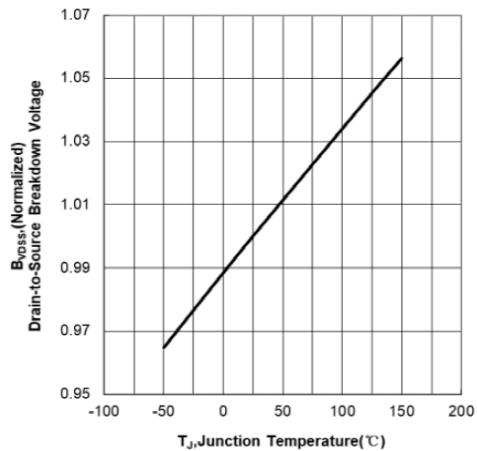


Fig 9 Normalized Breakdown Voltage  
vs. Junction Temperature

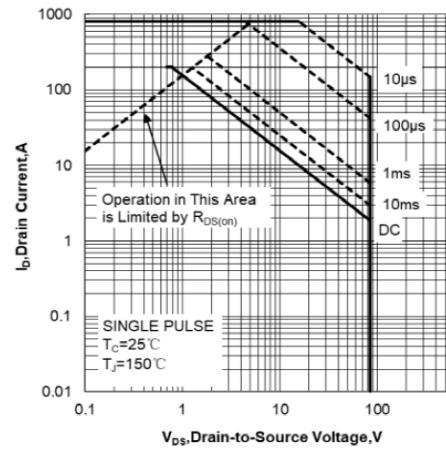


Fig 10 Safe Operation Area

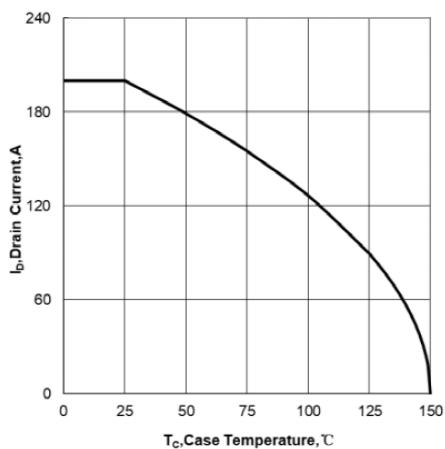


Figure 11 Maximum Continuous Drain Current  
vs. Case Temperature

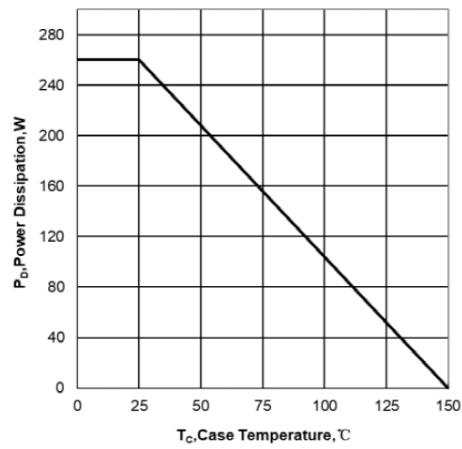
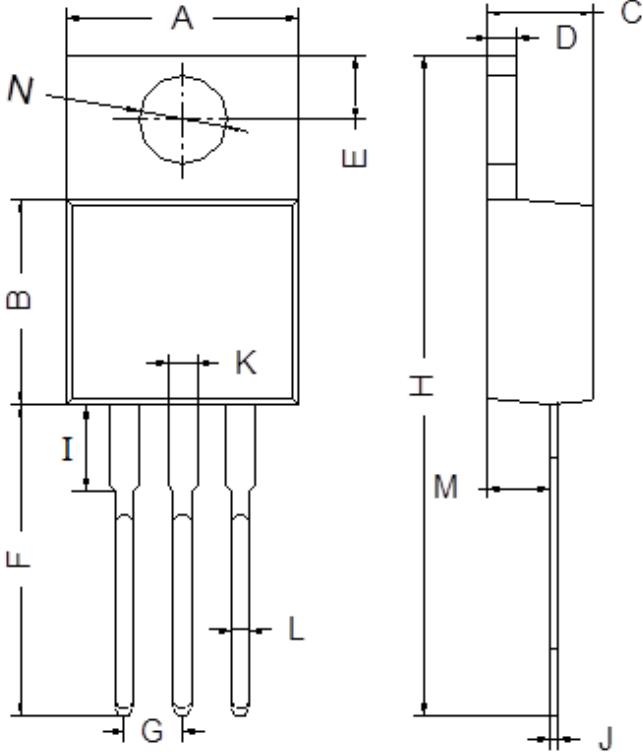


Figure 12 Maximum Power Dissipation  
vs. Case Temperature

**Package Outline Dimensions** (Unit: mm)



The diagram illustrates the TO-220AB package outline with various dimensions labeled:

- A:** Total width of the package body.
- B:** Total height of the package body.
- C:** Width of the lead foot.
- D:** Lead thickness.
- E:** Lead height.
- F:** Lead pitch.
- G:** Lead width at the base.
- H:** Total height of the package including leads.
- I:** Lead height from the bottom of the lead to the top of the lead foot.
- J:** Lead width at the base.
- K:** Lead height from the bottom of the lead to the top of the lead foot.
- L:** Lead width at the base.
- M:** Lead height from the bottom of the lead to the top of the lead foot.
- N:** Lead height from the bottom of the lead to the top of the lead foot.
- W:** Lead width at the base.

**TO-220AB**

Dimension	Min.	Max.
A	9.80	10.30
B	8.70	9.10
C	4.37	4.77
D	1.07	1.47
E	2.64	2.84
F	13.14	13.74
G	2.44	2.64
H	28.03	28.83
I	3.50	4.00
J	0.28	0.48
K	1.22	1.32
L	0.71	0.91
M	2.40	2.60
N	3.76	3.96

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