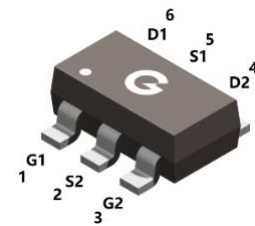
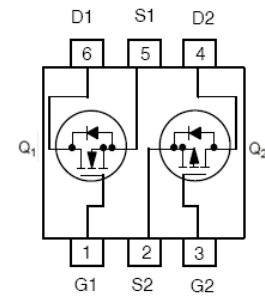


Features

- Low on-resistance
- Low input capacitance
- Fast switching speed
- Low input/output leakage
- Fast switching speed

HF



SOT-23-6L

Typical Applications

- Motor control
- Power Management Functions
- DC-DC Converters
- Backlighting

Mechanical Data

- Case: SOT-23-6L
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL2038-6L	SOT-23-6L	3000 pcs / Tape & Reel	2038

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Q1	Q2	Unit
Drain-to-Source Voltage	V _{DSS}	20	-20	V
Gate-to-Source Voltage	V _{GSS}	±12	±12	V
Continuous Drain Current (T _A = 25°C) ^{*1}	I _D	4.5	-3.2	A
Continuous Drain Current (T _A = 70°C) ^{*1}		3.6	-2.6	A
Pulsed Drain Current (t _p = 10μs, T _A = 25°C)	I _{DM}	25	-20	A
Single Pulse Avalanche Energy ^{*4}	E _{AS}	5	5	mJ
Power Dissipation (T _A = 25°C) ^{*1}	P _D	1.1		W
Power Dissipation (T _A = 25°C) ^{*2}		0.7		W
Operating Junction Temperature Range	T _J	-55 ~ +150		°C
Storage Temperature Range	T _{STG}	-55 ~ +150		°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Air ^{*1}	R _{θJA}	-	100	110	°C/W
Thermal Resistance Junction-to-Air ^{*2}		-	-	180	°C/W

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by surface mounted on a minimum recommended FR-4 board

Electrical Characteristics-Q₁ (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 20V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±12V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance ^{*3}	V _{GS} = 4.5V, I _D = 4A	-	23	35	mΩ
		V _{GS} = 2.5V, I _D = 2.5A	-	29	43	
		V _{GS} = 1.8V, I _D = 1A	-	51	75	
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250uA	0.4	0.8	1	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	9	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V	-	426	-	pF
C _{OSS}	Output Capacitance		V _{DS} = 10V	-	76	
C _{RSS}	Reverse Transfer Capacitance	f = 1.0MHz	-	66	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time ^{*5}	V _{DD} = 10V V _{GS} = 4.5V I _D = 3.6A R _G = 6Ω	-	9	-	ns
t _r	Turn-on Rise Time ^{*5}		-	23	-	
t _{d(OFF)}	Turn-Off Delay Time ^{*5}		-	38	-	
t _f	Turn-Off Fall Time ^{*5}		-	3	-	
Q _G	Total Gate-Charge	V _{DD} = 10V V _{GS} = 4.5V I _D = 3.6A	-	6.4	-	nC
Q _{GS}	Gate to Source Charge		-	1.1	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	1.8	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage ^{*3}	I _{SD} = 1A, V _{GS} = 0V	-	0.7	1.1	V

Electrical Characteristics-Q₂ (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250μA	-20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -16V, V _{GS} = 0V	-	-	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±12V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *3	V _{GS} = -4.5V, I _D = -3A	-	56	74	mΩ
		V _{GS} = -2.5V, I _D = -1.5A	-	77	110	
		V _{GS} = -1.8V, I _D = -1A	-	96	168	
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-0.4	-0.7	-1.0	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	27	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V	-	550	-	pF
C _{OSS}	Output Capacitance	V _{DS} = -10V	-	70	-	
C _{RSS}	Reverse Transfer Capacitance	f = 1.0MHz	-	61	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time *5	V _{DD} = -10V	-	11	-	ns
t _r	Turn-on Rise Time *5	V _{GS} = -4.5V	-	35	-	
t _{d(OFF)}	Turn-Off Delay Time *5	I _D = -1A	-	30	-	
t _f	Turn-Off Fall Time *5	R _G = 10Ω	-	10	-	
Q _G	Total Gate-Charge	V _{DD} = -10V	-	6.5	-	nC
Q _{GS}	Gate to Source Charge	V _{GS} = -4.5V	-	1.6	-	
Q _{GD}	Gate to Drain (Miller) Charge	I _D = -3.6A	-	1.4	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *3	I _{SD} = -0.6A, V _{GS} = 0V	-	-0.7	-1.0	V

Notes:

- The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
- The E_{AS} data shows Max. rating. N: The test condition is V_{DD} = 10V, V_{GS} = 6V, L = 0.1mH;
P: The test condition is V_{DD} = -10V, V_{GS} = -6V, L = 0.1mH
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves-Q₁ (@ T_A = 25°C unless otherwise specified)

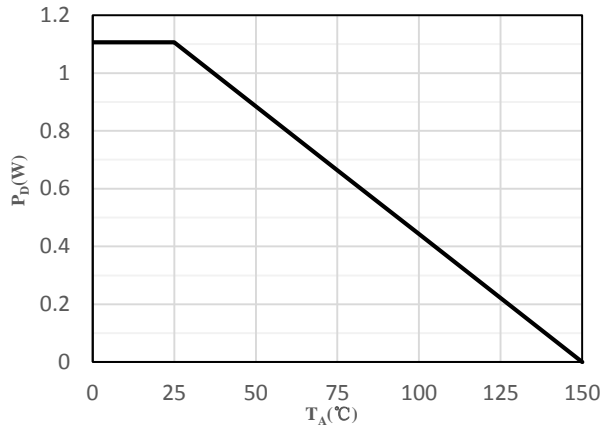


Fig 1 Power Dissipation

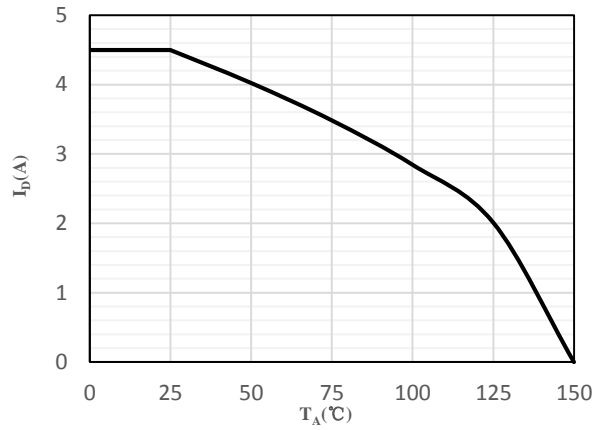


Fig 2 Drain Current

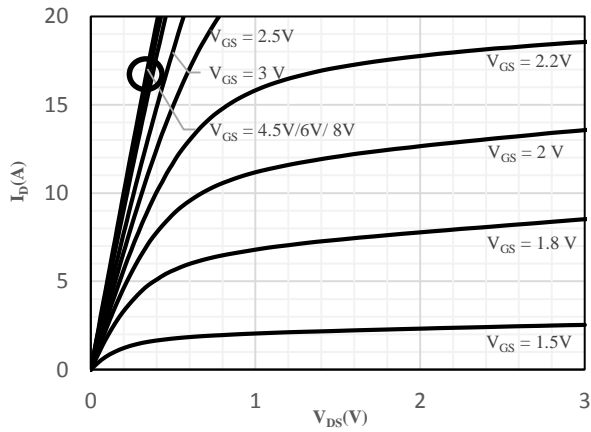


Fig 3 Typical Output Characteristics

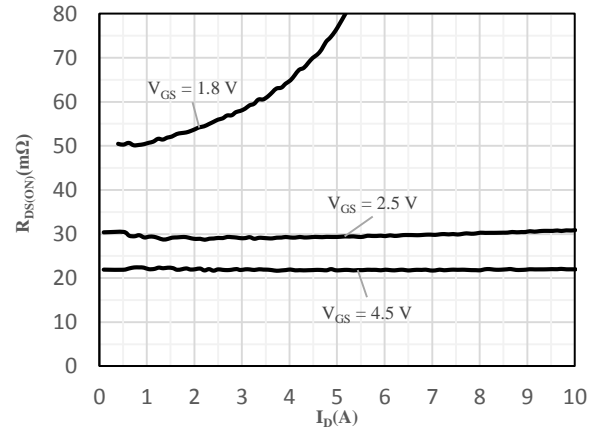


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

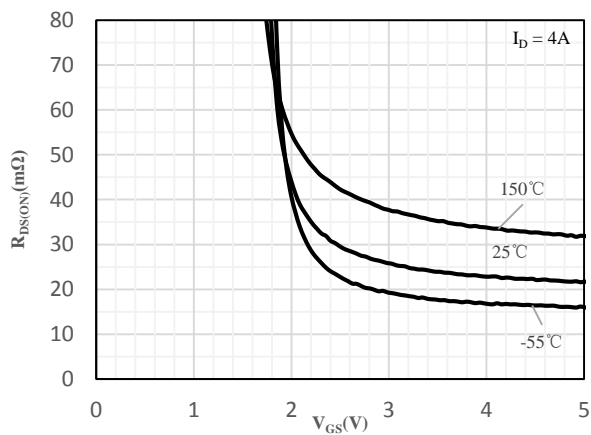


Fig 5 On-Resistance vs. Gate-Source Voltage

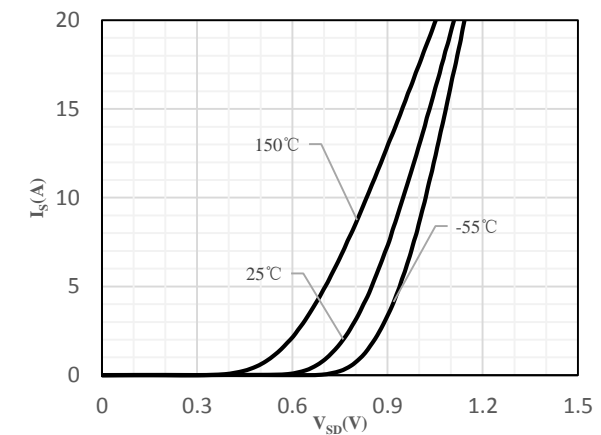


Fig 6 Body-Diode Characteristics

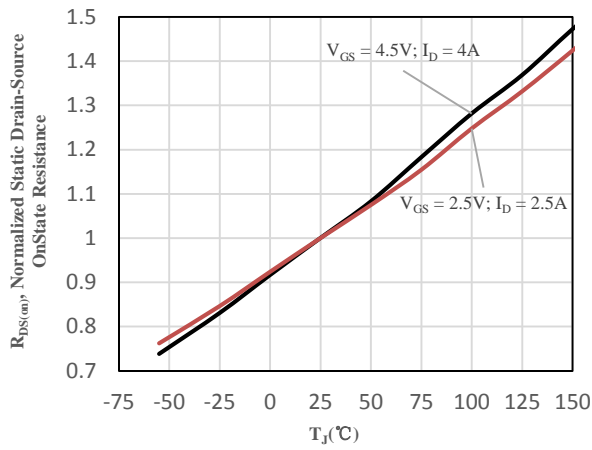


Fig 7 Normalized On-Resistance vs. Junction Temperature

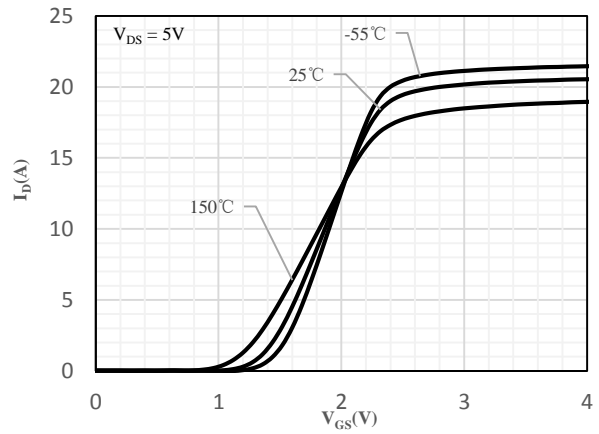


Fig 8 Transfer Characteristics

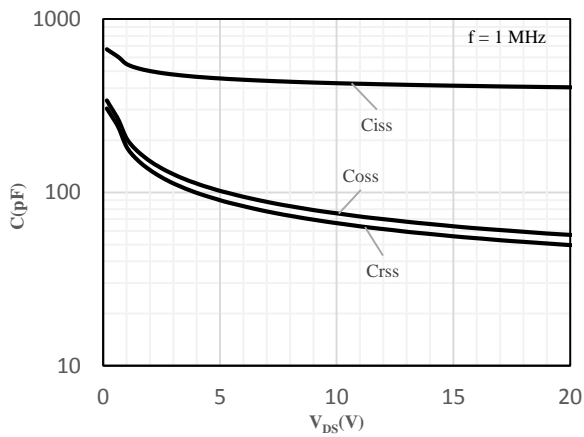


Fig 9 Capacitance Characteristics

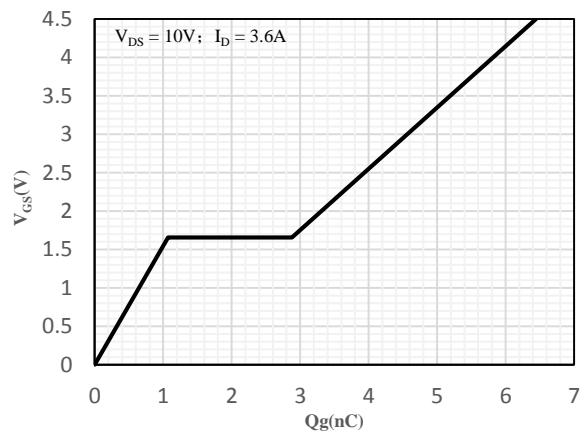


Fig 10 Gate-Charge Characteristics

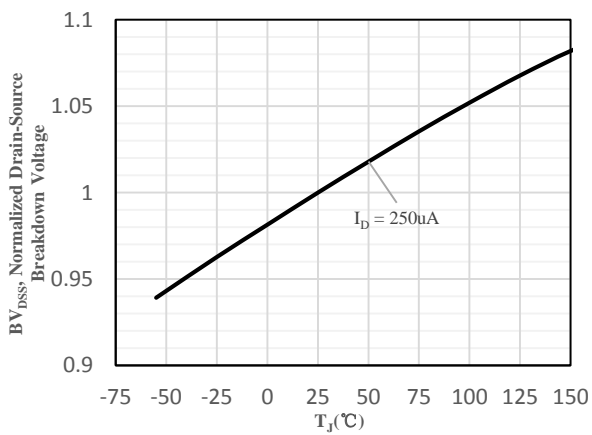


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

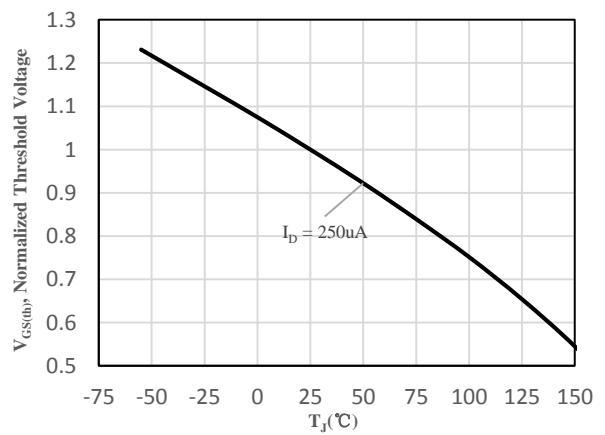


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Ratings and Characteristics Curves-Q₂ (@ T_A = 25°C unless otherwise specified)

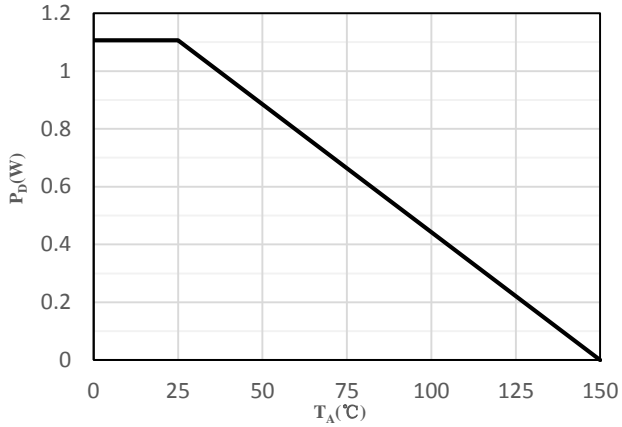


Fig 1 Power Dissipation

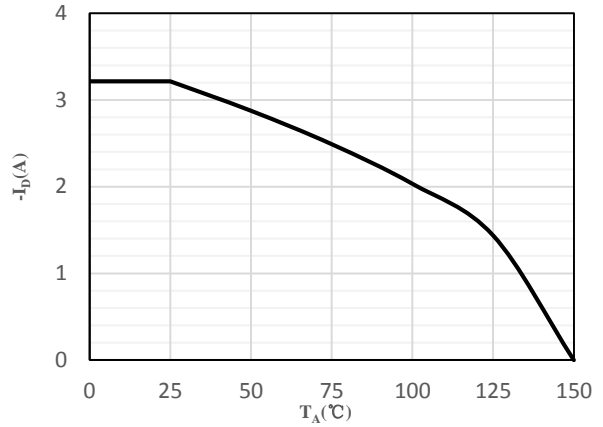


Fig 2 Drain Current

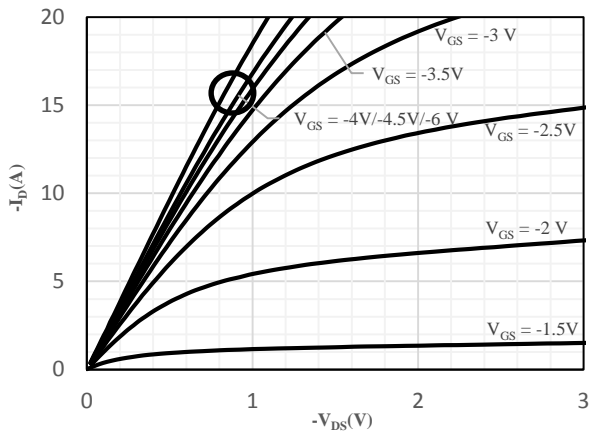


Fig 3 Typical Output Characteristics

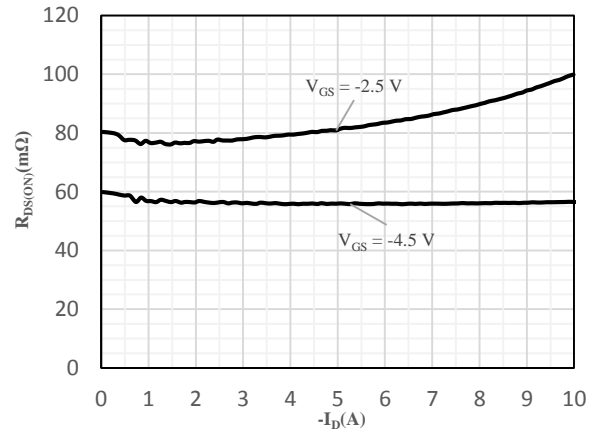


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

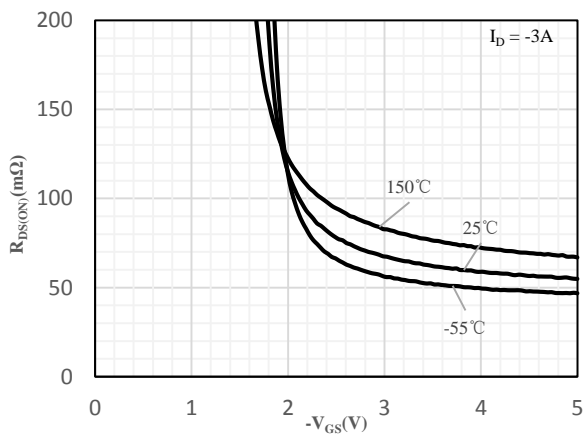


Fig 5 On-Resistance vs. Gate-Source Voltage

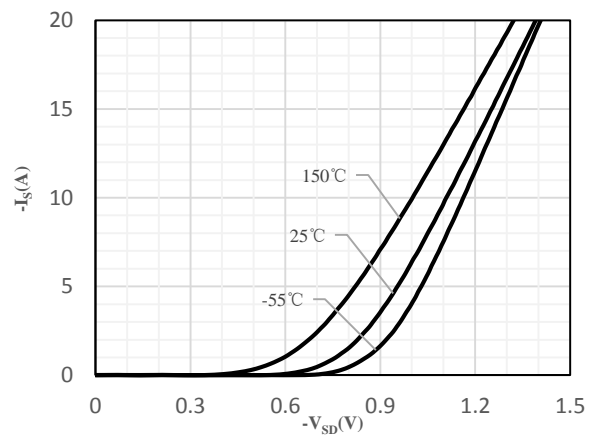


Fig 6 Body-Diode Characteristics

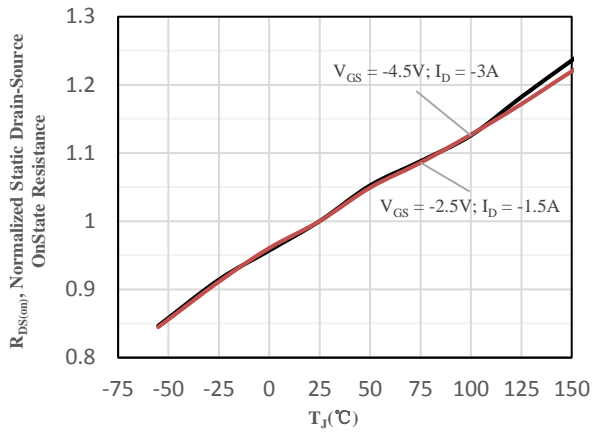


Fig 7 Normalized On-Resistance vs. Junction Temperature

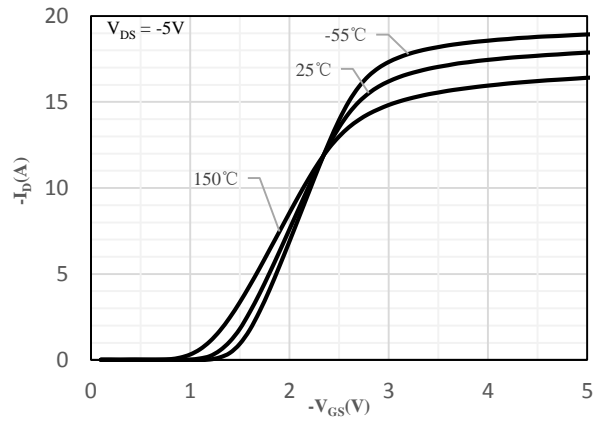


Fig 8 Transfer Characteristics

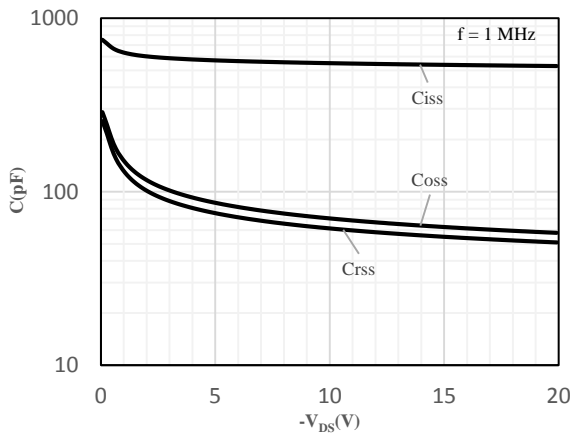


Fig 9 Capacitance Characteristics

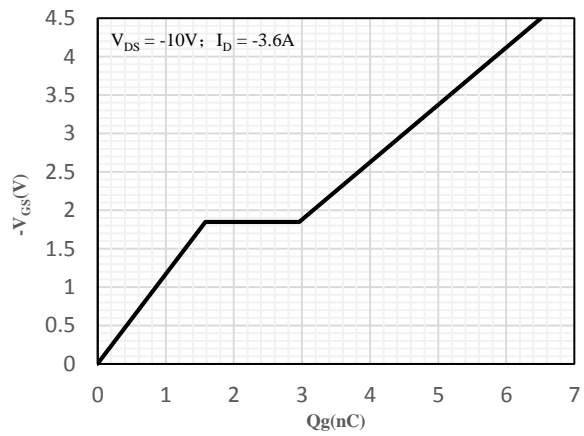


Fig 10 Gate-Charge Characteristics

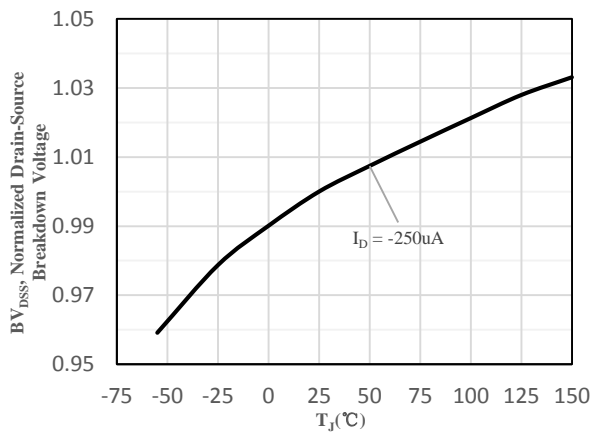


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

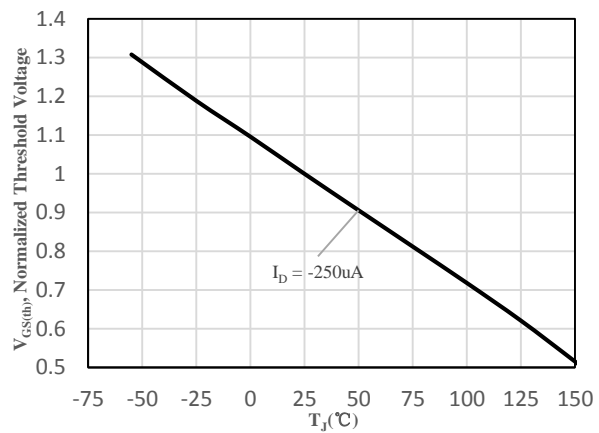
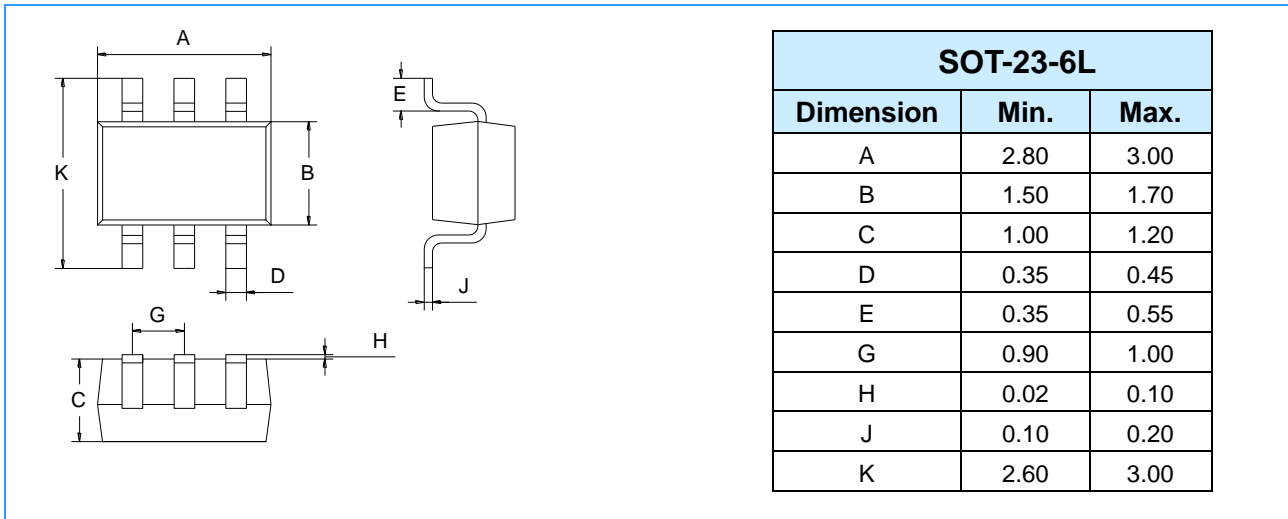
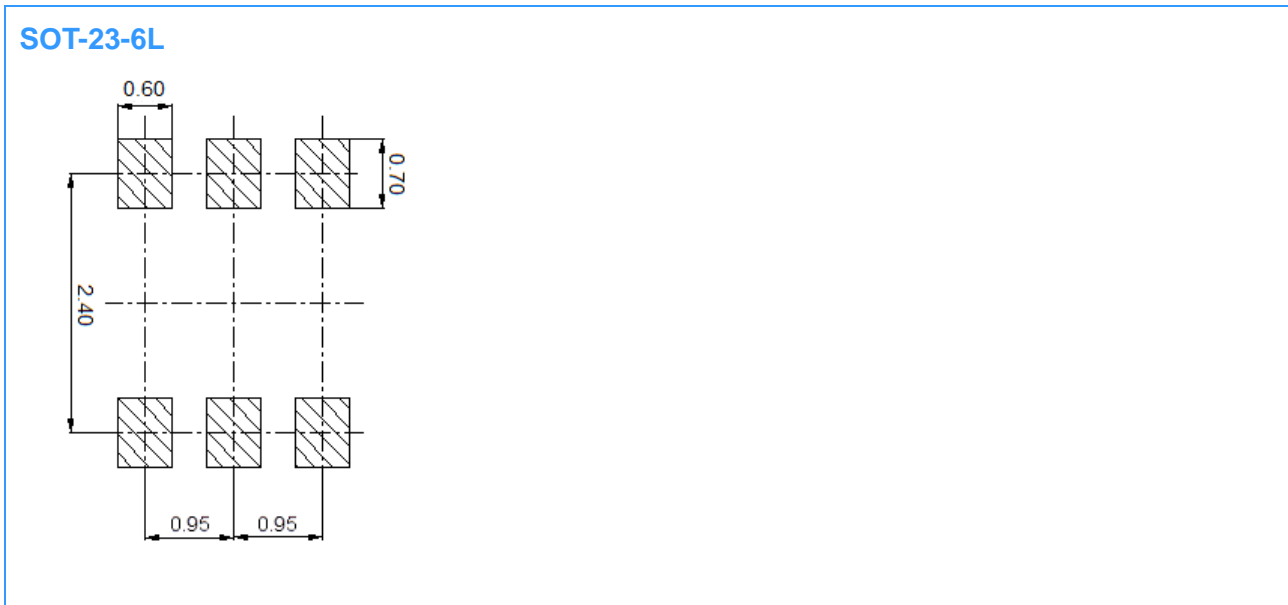


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Package Outline Dimensions (Unit: mm)



Package Outline Dimensions (Unit: mm)



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